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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE In re Patent Application of

C. OKAMOTO et al

Serial No. 10/673,451

Group Art Unit: 2876

Filed: September 30, 2003 Examiner: D. Lee

For: METHOD OF CHECKING AUTHENTICITY OF SHEET WITH

BUILT-IN ELECTRONIC CIRCUIT CHIP

REPLY

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 August 3, 2005

Sir:

In reply to the final rejection mailed October 4, 2004, please amend the above-identified application as set forth below. A Notice of Appeal was filed March 4, 2005, and a Petition for a Three-Month Extension of Time accompanies this Reply.